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## Amendments to the Claims:

Claim 1 (original): A circuit for current regulation, comprising:

- a regulation circuit coupled to a power supply and to a first node and configured to generate a regulated current;
- a first resistor coupled between the first node and a second node and configured to generate a signal relating to the regulated current that is supplied to a load;
- a second controller coupled to the second node and configured to generate a second control signal used to adjust the regulated current; and
- a controller having a first input coupled to the first node and a second input coupled to the second control signal and an output coupled to the regulation circuit, wherein the controller is configured to generate a control signal used to generate the regulated current.
- Claim 2 (original): The circuit of Claim 1, further comprising a reference voltage coupled to the first input and the regulation circuit.
- Claim 3 (original): The circuit of Claim 1, wherein the regulation circuit is a transistor that is coupled to the control signal generated by the controller.
- Claim 4 (original): The circuit of Claim 1, wherein the second control further comprises a second resistor and at least one current source.
- Claim 5 (original): The circuit of Claim 1, wherein the controller further comprises an amplifier.
- Claim 6 (original): The circuit of Claim 4, wherein the regulation circuit is a transistor that is coupled to an output of the amplifier.
- Claim 7 (original): The circuit of Claim 6, further comprising a reference voltage coupled to an input of the amplifier.

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Claim 8 (original): The circuit of Claim 4, wherein the at least one current source is activated in response to a predetermined condition.

Claim 9 (original): The circuit of Claim 8, wherein the predetermined condition is related to a temperature associated with the circuit.

Claim 10 (original): The circuit of Claim 8, wherein the amplifier is configured to generate the control signal such that the regulated current is driven to zero safely.

Claim 11 (original): An apparatus for current regulation, comprising:

- a transistor coupled to a power supply, a first node, and a second node;
- a sense resistor coupled between the first node and a third node;
- a second resistor coupled between the third node and a fourth node;
- a first current source coupled to the fourth node; and

an amplifier having a first input coupled to the first node and a second input coupled to a fourth node, and an output coupled to the second node.

Claim 12 (original): The apparatus of Claim 11, further comprising a voltage reference coupled to the first input of the amplifier.

Claim 13 (original): The apparatus of Claim 11, wherein the base of the transistor is coupled to the second node, the emitter is coupled to the power supply and the collector is coupled to the first node.

Claim 14 (original): The apparatus of Claim 11, further comprising a load coupled to the third node.

Claim 15 (original): The apparatus of Claim 11, further comprising at least one additional current source coupled to the fourth node.

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Claim 16 (currently amended): A method for current regulation of a circuit, comprising:

utilizing a regulation circuit that is coupled to a power supply and to a first node and that is configured to generate generating a regulated current that is supplied to a load;

sensing the regulated current and generating a signal relating to the regulated current that is supplied to a load; monitoring the circuit for a predetermined condition;

utilizing a second controller that is coupled to a second node and that is configured to activate activating a second control signal that is used to adjust the regulated current; by using at least one current source when the predetermined condition occurs; and

utilizing a controller including a first input coupled to the first node and a second input coupled to the second control signal and an output coupled to the regulation circuit, wherein the controller is configured to generate a control signal that is used to adjust the regulated current.

adjusting the regulated current using the at least one current source to vary the regulated current such that the regulated current may be safely driven to zero.

Claim 17 (original): The method of Claim 16, wherein activating the second control signal further comprises utilizing a resistor and the at least one current source.

Claim 18 (original): The method of Claim 17, wherein adjusting the regulated current further comprises utilizing a single amplifier.

Claim 19 (currently amended): An apparatus for current regulation, comprising: means for generating a regulated signal; means for sensing the regulated signal between a first node and a second node; means for generating a signal relating to the regulated signal that is supplied to a load; means for utilizing a second controller that is coupled to the second node and that is configured to generate a second control signal that is used to adjust the regulated signal; and means for utilizing a controller including a first input coupled to the first node and a second input coupled to the second control signal and an output coupled to the means for

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generating a regulated signal, wherein the controller is configured to generate a control signal that is used to adjust the regulated signal.

means for generating a second control signal-that-utilizes at least one current source; and means for adjusting a magnitude of the regulated signal-in response to the second control signal and the regulated signal.